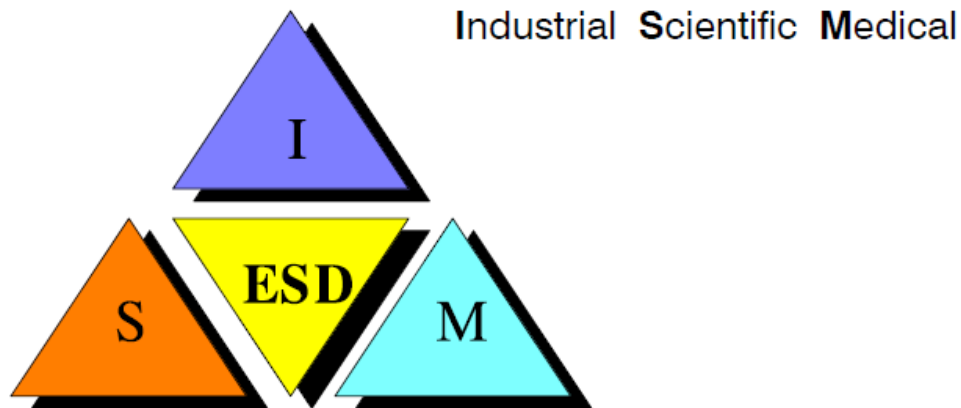


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**Master Program M.Sc.**

**“Embedded Systems Design [ESD]“**



**Module Handbook**

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### Explanations

#### Frequency of Modules:

All modules are offered once per year. In the module descriptions it is outlined if a module takes place in 1. semester (summer semester) or in 2. semester (winter semester).

#### Module duration:

All modules finish within one semester. Examinations are offered two times per year.

#### Workload:

A credit point (CP) corresponds to 30 hours of work (including self-learning).

Credits are earned after passing the academic records (exams and/or assessed assignments of the courses).

The compulsory course can be any course from a master program or from the list of general studies.

## **C. MODULE HANDBOOK**

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|  | <b>System Theory and Identification</b>                 | As of: January 2016 |

|                   |   |               |              |               |
|-------------------|---|---------------|--------------|---------------|
| Module Name       | <b>C.1. System Theory and Identification</b>  |               | Abbreviation | <b>SY-SID</b> |
| Module Group      | Systems   |               | mandat. [X]  | option []     |
| Summer / Winter   | Summer  | Semester Term | <b>1</b>     |               |
| Master Program    | ESD   |               |              |               |
| Group             | 15 students   |               |              |               |
| Teaching Staff    | K. Müller, K. Peter   |               |              |               |
| Person in Charge  | K. Peter  |               |              |               |
| Requirements      |   |               |              |               |
| Course Types      | Class   | 3 h           | GF = 0,5     |               |
|                   | Exercise / Lab  | 1 h           | GF = 0,5     |               |
| Course Objectives | <p>The module provides the skills for description and identification of complex systems (time continues and discrete) for electrical, mechanical and thermal systems.</p> <p>The students can</p> <ul style="list-style-type: none"> <li>- set up the differential equations (time continues and discrete) for dynamic systems and analyze the dynamic behavior</li> <li>- set up state-space descriptions (time continues and discrete)</li> <li>- can linearize non-linear systems</li> <li>- can transform systems form time domain to frequency domain for continues and discrete systems</li> <li>- apply parameter identification algorithms in order to determine parameters from measured values</li> <li>- apply numeric and symbolic software for modeling, simulation, analysis and identification of dynamic systems</li> </ul> |               |              |               |
| Content           | <ul style="list-style-type: none"> <li>- signals and systems (time continues and discrete)</li> <li>- description of the system in time domain: Differential equations and state-space representation (time continues and discrete) of non-linear systems, time-variant systems, time delay, linear systems, linearization, linear time-invariant systems (LTI)</li> <li>- description of the system in the frequency domain (time continues and discrete): Laplace Transform, Z-Transform, Autocorrelation, Cross-Correlation, DFT, FFT, Power-Spectral-Density (PSD)</li> <li>- parameter identification methods: Least square (DLS and RLS), Maximum Likelihood, test signals and test signal generation</li> </ul>  |               |              |               |
| Methods           | Class, Lab  |               |              |               |

|  |   |                     |
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|  | <b>System Theory and Identification</b>                 | As of: January 2016 |

|              |       |   |     |                          |             |          |
|--------------|-------|---|-----|--------------------------|-------------|----------|
| Literature   |       | Bernd Girod, Rudolf Rabenstein, Alexander Stenger: Einführung in die Systemtheorie. Teubner, 2003<br>Alan V. Oppenheim, Alan S. Willsky, S. Hamid Nawab: Signals and Systems. Prentice-Hall, 1996<br>Rolf Isermann: Identifikation dynamischer Systeme: Grundlegende Methoden, Springer, 1992<br>Heinz Unbehauen: Regelungstechnik III, Identifikation, Adaption, Optimierung, Vieweg+Teubner 2011<br>Alberto Isidori: Nonlinear Control Systems, Springer 1995 |     |                          |             |          |
| Exams        |       | Written or oral Examination   |     |                          |             |          |
| Workload (h) | class | Exercises / Seminars / other,   | lab | Home work / presentation | Preparation | Industry |
|              | 45    | 15  | 0   | 0                        | 90          | 0        |
| Language     |       | English   |     |                          |             |          |
| Remarks      |       |   |     |                          |             |          |
| Credits      |       | <b>5</b>  |     |                          |             |          |

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|--|---|---------------------|
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|  | <b>Mechatronics</b>                                     | As of: January 2016 |

|                   |  |               |   |                                 |
|-------------------|--|---------------|---|---------------------------------|
| Module Name       | <b>C.2. Mechatronics</b>   |               | Abbreviation                                | <b>SY-MEC</b>                   |
| Module Group      | Systeme  |               | mandat. <input checked="" type="checkbox"/> | option <input type="checkbox"/> |
| Summer / Winter   | Sommer   | Semester Term | <b>1</b>                                    |                                 |
| Master Program    | ESD  |               |   |                                 |
| Group             | 15 students  |               |   |                                 |
| Taeching Staff    | N. Buro, K. Peter, K. Müller   |               |   |                                 |
| Person in Charger | K. Peter   |               |   |                                 |
| Requirements      |  |               |   |                                 |
| Course Types      | Class  | 3 h           | GF = 1,0                                    |                                 |
|                   | Exercise / Lab   | 1 h           | SL  |                                 |
| Course Objectives | <p>The module provides the skills for modeling of electro-mechanical systems. It gives a deeper understanding of the properties of mechanical systems combined with electrical actuators for the design of controls with embedded systems.</p> <p>The students</p> <ul style="list-style-type: none"> <li>• can describe mechanical and electrical systems by differential equations</li> <li>• know the basic strategies for the control of electro-mechanical systems</li> <li>• understand the relationship between, electric fields, electric currents, magnetic fields and the forces</li> <li>• know the basic types of electro-mechanical actuators</li> <li>• know the basics of tribology</li> </ul>  |               |   |                                 |
| Content           | <ul style="list-style-type: none"> <li>• Hamiltonian mechanics</li> <li>• mechanical constraints: Holonomic constraints, Non-holonomic constraints, Pfaffian constraints, scleronomous constraints, rheonomous constraints</li> <li>• non-relativistic Lagrangian mechanics</li> <li>• Energy und coenergy, coordinate systems, Lagrange's equations, D'Alembert's principle, conserved momenta, energy conservation, conservative forces, methods to include dissipation and friction, methods to include non-conservative forces</li> <li>• Modeling of friction: Solid/boundary friction, mixed friction, fluid friction, Stribeck effect, sliding-contact bearings, roller bearings</li> <li>• Deriving the equations of motion for several examples</li> <li>• Basic modeling of electric drive systems: DC-drives, AC-drives, frequency converters, gears, linear drives</li> <li>• Simulation of simple controls of electro-mechanical systems: Effects of nonlinearities (e.g. Stribeck effect)</li> </ul> |               |   |                                 |
| Methods           | Class, Lab   |               |   |                                 |
| Literature        | Herbert Goldstein, Charles P. Poole, John L. Safko: Klassische Mechanik. Wiley-VCH, 2006<br>Cornelius Lanczos: The Variational Principles of Mechanics. Dover Publ. Inc., 1986   |               |   |                                 |

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|--|---|---------------------|
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|  | <b>Mechatronics</b>                                     | As of: January 2016 |

|              |       |  |     |                          |             |          |
|--------------|-------|--|-----|--------------------------|-------------|----------|
|              |       | F. Cellier: Continuous System Modeling, Springer Verlag, 1991<br>Landau, L.D./E.M. Lifshitz: Mechanics Volume 1 (Course of Theoretical Physics), Butterworth-Heinemann, Reprint of 1976<br>Feynman, R. P.: Lectures on Physics, Basic Books 2014 |     |                          |             |          |
| Exams        |       | Written or oral Examination  |     |                          |             |          |
| Workload (h) | class | Exercises / Seminars / other   | lab | Home work / presentation | Preparation | Industry |
|              | 45    | 7  | 8   | 0                        | 90          | 0        |
| Language     |       | English  |     |                          |             |          |
| Remarks      |       |  |     |                          |             |          |
| Credits      |       | <b>5</b>   |     |                          |             |          |

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|  | <b>Discrete Control Systems</b>                         | As of: January 2016 |

|                   |  |               |   |                                 |
|-------------------|--|---------------|---|---------------------------------|
| Module Name       | <b>C.3. Discrete Control Systems</b>   |               | Abbreviation                                | <b>AU-DCS</b>                   |
| Module Group      | Systems  |               | mandat. <input checked="" type="checkbox"/> | option <input type="checkbox"/> |
| Summer / Winter   | Summer   | Semester Term | <b>1</b>                                    |                                 |
| Master Program    | ESD  |               |   |                                 |
| Group             | 15 students  |               |   |                                 |
| Teaching Staff    | K. Peter, K. Müller  |               |   |                                 |
| Person in Charge  | K. Peter   |               |   |                                 |
| Requirements      |  |               |   |                                 |
| Course Types      | Class  | 3 h           | GF = 1,0                                    |                                 |
|                   | Exercise / Lab   | 1 h           | SL  |                                 |
| Course Objectives | <p>The module provides the skills for designing sophisticated controls.</p> <p>The students</p> <ul style="list-style-type: none"> <li>• can design state-feedback controls, PI-state-feedback controls</li> <li>• can analyse the stability of control systems and can distinguish between BIBO-, BIBS- and lyapunov stability</li> <li>• can design state-observers</li> <li>• can design combinations of observers and state-controls</li> <li>• can design optimal controls and optimal observers (LQRs, LQEs and LQGs)</li> <li>• can design MIMO controls</li> </ul>   |               |   |                                 |
| Contents          | <ul style="list-style-type: none"> <li>• state-space representation of dynamic systems: D'Alembert's principle, differential equation, linearization, state-space (time continues and discret), continues to discrete transformations (ZOH, bilinear transformation), eigenvalues, eigenvectors, solutions for IVPs, canonical forms, Jordan form, trajectories, Cayley–Hamilton theorem, transfer function</li> <li>• stability: BIBO-, BIBS- and lyapunov stability</li> <li>• state-feedback controls: Pole placement, Ackermanns formula, PI-state-feedback controls,</li> <li>• state-observers (time continues and discret), discrete parallel model, combinations of observers and state-controls</li> <li>• Optimal controls: LQR design, Cost function, Matrix-Riccati Equation, solution by Hamiltonian matrix,</li> <li>• Optimal observers: LQEs, Kalman filtering for noise suppression, reliability of measurements, LQGs</li> <li>• MIMO controls: Full modal synthesis (according to Roppenecker)</li> </ul> |               |   |                                 |
| Methods           | Class, Lab   |               |   |                                 |
| Literature        | <p>Karl Johan Aström, Richard M. Murray: Feedback Systems, Princeton University Press 2008</p> <p>Franklin, G. F.; Powell, J. D.; Emami-Naeini, A.: <i>Feedback Control of Dynamic Systems</i>, Prentice Hall, 2002</p> <p>Ludyk, G.: <i>Theoretische Regelungstechnik 1 u. 2</i>, Springer-Verlag, 1995</p> <p>Unbehauen, H.: <i>Regelungstechnik I, II u. III</i>, Vieweg, 1998</p> <p>Föllinger, O.: <i>Regelungstechnik</i>, Hüthig, 1994</p>  |               |   |                                 |

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|--|---|--|---------------------|
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|  | <b>Discrete Control Systems</b>                         |  | As of: January 2016 |

|              |       |  |      |                          |             |          |
|--------------|-------|--|------|--------------------------|-------------|----------|
|              |       | <p>O. Föllinger, G. Roppenecker: Optimale Regelung und Steuerung, Dezember 1994<br/>         von Otto Föllinger (Autor), Günter Roppenecker (Mitwirkende)<br/>         Maciejowski, J. M.: Multivariable Feedback Design.<br/>         Addison-Wesley, Wokingham, England, 1989<br/>         Li Tan: Digital Signal Processing: Fundamentals and Applications.<br/>         Academic Press, 2007</p> |      |                          |             |          |
| Exams        |       | written or oral exam   |      |                          |             |          |
| Workload (h) | class | seminars/ others   | labs | Home work / presentation | preparation | industry |
|              | 45    | 0  | 15   | 0                        | 90          | 0        |
| Language     |       | English  |      |                          |             |          |
| Remarks      |       |  |      |                          |             |          |
| Credits      |       | <b>5</b>   |      |                          |             |          |



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|--|---|---------------------|
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|  | <b>Digital Systems / VHDL</b>                           | As of: January 2016 |

|                   |   |               |              |               |
|-------------------|---|---------------|--------------|---------------|
| Module Name       | <b>C.4. Digital Systems / VHDL</b>  |               | Abbreviation | <b>ET-DTV</b> |
| Module Group      | Digital Systems   |               | mandat. [X]  | option []     |
| Summer/Winter     | Summer Term   | Semester Term | <b>1</b>     |               |
| Master Program    | ESD   |               |              |               |
| Group             | 15 students   |               |              |               |
| Teaching Staff    | K. Mueller  |               |              |               |
| Person in Charge  | K. Mueller  |               |              |               |
| Requirements      |   |               |              |               |
| Course Types      | Class (2 h) , Lab (2 h)   |               |              |               |
| Course Objectives | <p>The module deepens the knowledge on digital systems and enables the students to design, simulate und implement programmable logic using VHDL. The students</p> <ul style="list-style-type: none"> <li>• can design complex sequential logic</li> <li>• handle optimization and minimization of digital logic</li> <li>• know microprocessor architectures and can develop programs in assembly and C/C++ language</li> <li>• know the elements of VHDL can implement logic systems on FPGAs</li> </ul>   |               |              |               |
| Contents          | <ul style="list-style-type: none"> <li>• elements of digital systems</li> <li>• sequential systems, state machine graphs</li> <li>• CISC- und RISC-architectures, DSPs</li> <li>• memory and memory controllers</li> <li>• CPLDs und FPGAs internals</li> <li>• VHDL programming und applications</li> <li>• communication protocols</li> </ul>   |               |              |               |
| Methods           | class, labs   |               |              |               |
| Literature        | <p>K. Urbanski u. R. Woitowitz: Digitaltechnik. Springer, 2000<br/> J. Wakerly: Digital Design: Principles and Practices. Prentice-Hall, 1999<br/> S. Yalamanchili: VHDL Starter's Guide. Prentice-Hall, 1998<br/> P. J. Ashenden: The Designer's Guide to VHDL. Elsevier/Morgan Kaufmann, 2011<br/> V. A. Pedroni: Circuit Design and Simulation with VHDL. MIT Press, 2010<br/> R. Lipsett, C. Schaefer and C. Ussery: VHDL: Hardware Description and Design. Kluwer Academic Publishers, 1990<br/> Xilinx Vivado Users's Guide. Xilinx Corp., 2015</p> |               |              |               |
| Exams             | written or oral exam  |               |              |               |

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|--|---|--|---------------------|
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|  | <b>Digital Systems / VHDL</b>                           |  | As of: January 2016 |

| Workload<br>(h) | class    | seminars/ others | labs | Home work /<br>presentation | preparation | industry |
|-----------------|----------|------------------|------|-----------------------------|-------------|----------|
|                 | 30       | 0                | 30   | 0                           | 90          | 0        |
| Language        | English  |                  |      |                             |             |          |
| Remarks         |          |                  |      |                             |             |          |
| Credits         | <b>5</b> |                  |      |                             |             |          |

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|--|---|---------------------|
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|  | <b>System-on-Chip-Design</b>                            | As of: January 2016 |

|                   |   |                |   |                                 |                      |
|-------------------|---|----------------|---|---------------------------------|----------------------|
| Module Name       | <b>C.5. System-on-Chip-Design</b>   |                |   | Abbreviation                    | <b>SY-SOC</b>        |
| Module Group      | Digital Systems / Computer Science  |                | mandat. <input checked="" type="checkbox"/> | option <input type="checkbox"/> |                      |
| Summer / Winter   | Summer Term   | Semester Term  | <b>1</b>                                    |                                 |                      |
| Master Program    | ESD   |                |   |                                 |                      |
| Group             | 15 students   |                |   |                                 |                      |
| Teaching Staff    | K. Mueller  |                |   |                                 |                      |
| Person in Charge  | K. Mueller  |                |   |                                 |                      |
| Requirements      |   |                |   |                                 |                      |
| Course Types      | class (2 h), lab (2 h)  |                |   |                                 |                      |
| Course Objectives | <p>The complete digital logic and control of systems can be included on a single device (System-on-Chip design). This results in very high speed, very reliable solutions at low cost. This module teaches all required techniques to create SoCs with custom logic, microcontrollers and the required interfaces. The students</p> <ul style="list-style-type: none"> <li>• can integrate custom logic and interfaces on a single chip</li> <li>• handle integration von microcontrollers (embedded softcore)</li> <li>• can solve control problems in hardware and software.</li> </ul> |                |   |                                 |                      |
| Contents          | <ul style="list-style-type: none"> <li>• Interfaces to sensors and actuators</li> <li>• AD and DA converters</li> <li>• advanced communication protocols</li> <li>• intellectual properties</li> <li>• embedded microcontroller, 8 bit, 32 bit</li> <li>• integration of digital components to a complete control system</li> <li>• Application examples in the industrial/scientific/medical area</li> </ul>   |                |   |                                 |                      |
| Methods           | Class, Labs   |                |   |                                 |                      |
| Literature        | <p>J. Wakerly: Digital Design: Principles and Practices. Prentice-Hall, 1999<br/> Xilinx PicoBlaze™ Users's Guide. Xilinx Corp., 2014<br/> Xilinx Vivado™ Users's Guide. Xilinx Corp., 2015<br/> Pong P. Chu: FPGA Prototyping By VHDL Examples (Xilinx Spartan-3 Version) Wiley Interscience, 2008<br/> R. Reis, M. Lubaszewski, J.A.G. Jess: Design of Systems on a Chip: Design and Test Springer 2010<br/> B.M. Al-Hashimi: System-on-Chip: Next Generation Electronics (Circuits, Devices and Systems) Insttit. of Eng. and Technology, 2006</p>                                     |                |   |                                 |                      |
| Exams             | written or oral exam  |                |   |                                 |                      |
| Workload          | class   | Seminar/others | labs  | homework/presentation           | preparation industry |

|  |   |  |                     |
|--|---|--|---------------------|
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|  | <b>System-on-Chip-Design</b>                            |  | As of: January 2016 |

|          |          |   |    |   |    |   |
|----------|----------|---|----|---|----|---|
| (h)      | 30       | 0 | 30 | 0 | 90 | 0 |
| Language | English  |   |    |   |    |   |
| Remarks  |          |   |    |   |    |   |
| Credits  | <b>5</b> |   |    |   |    |   |

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|--|---|---------------------|
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|  | <b>Model Based Software Development/<br/>Real-time Software</b> | As of: January 2016 |

|                   |   |               |   |                                 |
|-------------------|---|---------------|---|---------------------------------|
| Module Name       | <b>C.6. Model Based Software Development/<br/>Real-time Software</b>  |               | Abbreviation                                | <b>IT-MRT</b>                   |
| Module Group      | Informatics   |               | mandat. <input checked="" type="checkbox"/> | option <input type="checkbox"/> |
| Summer / Winter   | Summer  | Semester Term | <b>2</b>                                    |                                 |
| Master Program    | ESD   |               |   |                                 |
| Group             | 15 students   |               |   |                                 |
| Teaching Staff    | M. Lindemann, T. Umland   |               |   |                                 |
| Person in Charge  | K. Müller   |               |   |                                 |
| Requirements      |   |               |   |                                 |
| Course Types      | class   | 2 h           | GF = 1,0                                    |                                 |
|                   | Exercise / Lab  | 2 h           | SL  |                                 |
| Course Objectives | <p>The module provides the skills for designing real-time software. This includes the skills in model based software development.</p> <p>The students</p> <ul style="list-style-type: none"> <li>• know the principles of parallel data processing</li> <li>• understand typical failures in parallel data processing and understand the need for formal proofs for parallel algorithms</li> <li>• can develop synchronization concepts as „Monitor“, „Semaphore“ and „CSP“ and are able to develop solutions for parallel data processing in programming languages</li> <li>• understand the need for real-time signal processing</li> <li>• know the principles of hardware and software interrupts, interrupt controllers and interrupt handling</li> <li>• understand typical failures in real-time data processing</li> <li>• understand the principals of real-time operating systems</li> </ul>  |               |   |                                 |
| Contents          | <ul style="list-style-type: none"> <li>• motivation for parallel data processing</li> <li>• examples for parallel/distributed algorithms</li> <li>• examples of failures (dead-lock, priority inversion)</li> <li>• theoretical description of parallel models in state-space</li> <li>• proof for mandatory properties of parallel systems with „model checkers“</li> <li>• comparison of synchronization concepts „Monitor“, „Semaphor“ and „Communicating Sequentiell Processes (CSP)“</li> <li>• rules for transformation of the theoretical models into in programming language</li> <li>• programming of examples of synchronization concepts</li> <li>• need of real-time signal processing for control applications, discrete integration and differentiation in control loops</li> <li>• hardware and software interrupts, interrupt controllers and interrupt handling</li> <li>• known multi-processor concepts in servo drive applications: Inter-Processor Communication (IPC), Dual-Port-RAM</li> </ul> |               |   |                                 |

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|--|---|---------------------|
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|  | <b>Model Based Software Development/<br/>Real-time Software</b> | As of: January 2016 |

|              |   |   |                 |                                 |                   |                    |
|--------------|---|---|-----------------|---------------------------------|-------------------|--------------------|
|              | <ul style="list-style-type: none"> <li>• real-time operating systems: QNX, RTLinux</li> <li>• Networks and Real-Time Ethernet</li> </ul>  |   |                 |                                 |                   |                    |
| Methods      | Class, lab  |   |                 |                                 |                   |                    |
| Literature   | <p>E. W. Dijkstra: Cooperating sequential processes. In: F. Genys (Ed.), Programming Languages, Academic Press, New York (1968) 43-112</p> <p>P. B. Hansen, Java's insecure parallelism, ACM SIG-PLAN Notices, (4) 23 (1999) 38-45.</p> <p>C. A. R. Hoare: Monitors: An operating system structuring concept, Communications of the ACM, (10) 17 (1974), 549-557.</p> <p>C. A. R. Hoare: Communicating sequential processes, Communications of the ACM, (8) 21 (1978), 666-677.</p> <p>D. Lea: Concurrent Programming in Java - Design Principles and Patterns, The Java Series, Addison-Wesley, Reading, Massachusetts, 2. Auflage (2000).</p> <p>J. Magee, J. Kramer: Concurrency - State Models and Java Programs, John Wiley &amp; Sons, West Sussex, 2. Auflage (2006).</p> <p>B. Sanden: Coping with java threads, IEEE Computer, (4) 37 (2004), 20-27.</p> <p>B. Goetz: Java Concurrency in Practice, Addison-Wesley, Upper Saddle River, New Jersey (2006).</p> <p>T. Rauber, G. Runger: Parallele Programmierung, Springer-Verlag, Berlin, Heidelberg, 2. Auflage (2007).</p> |   |                 |                                 |                   |                    |
| Exams        | written or oral exam,   |   |                 |                                 |                   |                    |
| Workload (h) | class   | Übungen, Seminar, sonstige Kontaktstunden | Labor-Praktikum | Hausarbeit/Referat/Masterarbeit | Vor- und Nachber. | Industriepraktikum |
|              | 30  | 0   | 30              | 0                               | 90                | 0                  |
| Language     | English   |   |                 |                                 |                   |                    |
| Remarks      |   |   |                 |                                 |                   |                    |
| Credits      | <b>5</b>  |   |                 |                                 |                   |                    |

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|  | <b>Industrial Systems</b>                               | As of: January 2016 |

|                   |  |                  |          |   |                                 |               |
|-------------------|--|------------------|----------|---|---------------------------------|---------------|
| Module Name       | <b>C.7. Industrial Systems</b>   |                  |          |   | Abbreviation                    | <b>ES-IND</b> |
| Module Group      | Systems  |                  |          | mandat. <input checked="" type="checkbox"/> | option <input type="checkbox"/> |               |
| Summer / Winter   | Winter   |                  |          | Semester Term                               | <b>2</b>                        |               |
| Master Program    | ESD  |                  |          |   |                                 |               |
| Group             | 15 students  |                  |          |   |                                 |               |
| Teaching Staff    | K. Mueller K. Peter, n.n.  |                  |          |   |                                 |               |
| Person in Charge  | K. Mueller   |                  |          |   |                                 |               |
| Requirements      |  |                  |          |   |                                 |               |
| Course Types      | class  | 2 h              | GF = 0,5 |   |                                 |               |
|                   | Lab  | 2 h              | GF = 0,5 |   |                                 |               |
| Course Objectives | The module enable the students to design and implement typical embedded systems for industrial applications. This covers electrical drives systems, sensors and signal processing for power electronics.   |                  |          |   |                                 |               |
| Contents          | <p>Industrial systems will presented in detail. The applications are:</p> <ul style="list-style-type: none"> <li>• electrical drives (stepper motor, BLDC, AC-servo drive)</li> <li>• field-oriented control scheme</li> <li>• digital signal processing for control and power electronics</li> <li>• hardware/software realization</li> <li>• building automation</li> <li>• motor management</li> <li>• robotics</li> <li>• tool machines</li> <li>• condition-monitoring</li> <li>• real-time networks</li> </ul> |                  |          |   |                                 |               |
| Methods           | class, labs  |                  |          |   |                                 |               |
| Literature        | <p>Leonhard W.: Control of electrical Drives. Springer, 1997</p> <p>Isermann, R.: Mechatronische Systeme, Springer, 2008</p> <p>System Generator for DSP User's Guide, Xilinx Corp., 2011</p> <p>System Generator for DSP Reference Guide, Xilinx Corp., 2011</p>  |                  |          |   |                                 |               |
| Exams             | written or oral exam   |                  |          |   |                                 |               |
| Workload (h)      | class  | Seminars / other | lab      | Home work/ presentation                     | preparation                     | Industry      |
|                   | 30   | 0                | 30       | 0   | 90                              | 0             |
| Language          | English  |                  |          |   |                                 |               |
| Remarks           |  |                  |          |   |                                 |               |
| Credits           | <b>5</b>   |                  |          |   |                                 |               |

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|  | <b>Medical Systems</b>                                  | As of: January 2016 |

|                   |  |                  |          |   |                                 |               |
|-------------------|--|------------------|----------|---|---------------------------------|---------------|
| Module Name       | <b>C.8. Medical Systems</b>  |                  |          |   | Abbreviation                    | <b>ES-MED</b> |
| Module Group      | Systems  |                  |          | mandat. <input checked="" type="checkbox"/> | option <input type="checkbox"/> |               |
| Summer / Winter   | Winter   |                  |          | Semester Term                               | <b>2</b>                        |               |
| Master Program    | ESD  |                  |          |   |                                 |               |
| Group             | 15 students  |                  |          |   |                                 |               |
| Teaching Staff    | K. Mueller, K. Peter   |                  |          |   |                                 |               |
| Person in Charge  | K. Mueller   |                  |          |   |                                 |               |
| Requirements      |  |                  |          |   |                                 |               |
| Course Types      | Class  | 2 h              | GF = 0,5 |   |                                 |               |
|                   | Lab  | 2 h              | GF = 0,5 |   |                                 |               |
| Course Objectives | The module enables the students to design typical embedded systems for medical applications.   |                  |          |   |                                 |               |
| Contents          | <p>Embedded medical devices for diagnosis and treatment will be presented in technical detail:</p> <ul style="list-style-type: none"> <li>• blood pressure devices</li> <li>• oximetry</li> <li>• ECG/EEG systems and signal analysis, cardiac rhythm management</li> <li>• digital x-ray</li> <li>• ultrasonic actuation and measurement, flow measurement</li> <li>• flow cytometry, impedance tomography (EIT)</li> <li>• digital signal processing for medical signals <ul style="list-style-type: none"> <li>- FFT, IFFT</li> <li>- IIF and FIR filter design and high speed implementation</li> </ul> </li> <li>• automatic code generation</li> </ul> |                  |          |   |                                 |               |
| Methods           | class, lab   |                  |          |   |                                 |               |
| Literature        | <p>Northrop, R.: Noninvasive Instrumentation and Measurement in Medical Diagnosis (Biomedical Engineering)<br/>CRC Press, 2002</p> <p>Prutchi, D. und Norris, M.: Design and Development of Medical Electronic Instrumentation: A Practical Perspective of the Design, Construction, and Test of Medical Devices.<br/>John Wiley &amp; Sons, 2005</p>  |                  |          |   |                                 |               |
| Exams             | written or oral exam   |                  |          |   |                                 |               |
| Workload (h)      | class  | seminars / other | labs     | Home work / presentation                    | preparation                     | industry      |
|                   | 30   | 0                | 30       | 0   | 90                              | 0             |
| Language          | English  |                  |          |   |                                 |               |
| Remarks           |  |                  |          |   |                                 |               |
| Credits           | <b>5</b>   |                  |          |   |                                 |               |



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|  | <b>Maritime Scientific Systems</b>                      | As of: January 2016 |

|                    |   |                  |               |                          |              |               |
|--------------------|---|------------------|---------------|--------------------------|--------------|---------------|
| Module Name        | <b>C.9. Maritime Scientific Systems</b>   |                  |               |                          | Abbreviation | <b>ES-MAR</b> |
| Module Group       | Systems   |                  |               | mandat. [X]              | opt. [X]     |               |
| Summer / Winter    | Winter  |                  | Semester Term |                          | <b>2</b>     |               |
| Master Program     | ESD   |                  |               |                          |              |               |
| Group              | 15 students   |                  |               |                          |              |               |
| Teaching Staff     | A. Bochert  |                  |               |                          |              |               |
| Person in Charge   | K. Müller   |                  |               |                          |              |               |
| Requirements       |   |                  |               |                          |              |               |
| Course Types       | Class   | 2 h              |               | GF = 0,5                 |              |               |
|                    | Lab   | 2 h              |               | GF = 0,5                 |              |               |
| Course Objectives  | The module provides the skills for designing and applying embedded systems for scientific maritime systems.   |                  |               |                          |              |               |
| Contents           | <p>Maritime measurement and control systems will be taught in detail. Typical applications are:</p> <ul style="list-style-type: none"> <li>• remotely operated vehicles (ROV)</li> <li>• autonomous underwater vehicles (AUV)</li> <li>• measurement systems for environmental condition over and under water</li> <li>• floater/lander</li> </ul>                        |                  |               |                          |              |               |
| Methods            | Class, Lab  |                  |               |                          |              |               |
| Literature         | <p>Christ, D. und Wernli, L.: The ROV Manual: A User Guide for Observation Class Remotely Operated Vehicles. Butterworth Heinemann/Elsevier, 2007</p> <p>Richard L. Miller, R., Del Castillo, C. und McKee, B: Remote Sensing of Coastal Aquatic Environments: Technologies, Techniques and Applications (Remote Sensing and Digital Image Processing) Springer, 2005</p> |                  |               |                          |              |               |
| Exams              | written or oral exam  |                  |               |                          |              |               |
| Workload (Stunden) | class   | seminars / other | labs          | Home work / presentation | preparation  | industry      |
|                    | 30  | 0                | 30            | 0                        | 90           | 0             |
| Language           | English   |                  |               |                          |              |               |
| Remarks            |   |                  |               |                          |              |               |
| Credits            | <b>5</b>  |                  |               |                          |              |               |

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|  | <b>Safety and Reliability</b>                           | As of: January 2016 |

|                   |   |               |              |               |
|-------------------|---|---------------|--------------|---------------|
| Module Name       | <b>C.10. Safety and Reliability</b>   |               | Abbreviation | <b>ES-SAR</b> |
| Module Group      | Systems   |               | mandat. [X]  | option []     |
| Summer / Winter   | Winter  | Semester Term | <b>2</b>     |               |
| Group             | ESD   |               |              |               |
| Teaching Staff    | K. Peter  |               |              |               |
| Person in Charge  | K. Peter  |               |              |               |
| Requirements      |   |               |              |               |
| Course Types      | class (1h), lab (1 h)   |               |              |               |
| Course Objectives | <p>The module provides the skills for designing, assessing and certifying of safety critical systems</p> <p>The students</p> <ul style="list-style-type: none"> <li>• can design safety critical systems</li> <li>• can plan and apply safety critical development processes</li> <li>• can assess safety critical systems</li> <li>• can create a safety analysis</li> <li>• can create a reliability analysis</li> <li>• know the certification process for safety critical systems</li> <li>• know how to enhance safety</li> <li>• know how to enhance reliability</li> </ul>   |               |              |               |
| Contents          | <ul style="list-style-type: none"> <li>• basic probability calculations (distribution functions, Gaussian-, chi-square- Weibull- and exponential, confidence radius, median, safety and reliability benchmarks, boolean models, fault trees, dormant failures, average risk)</li> <li>• architecture of safety critical systems (dissimilar independence, separation and segregation, zonal analysis, common mode failures, active and passive redundancy)</li> <li>• safety and reliability project planning (determination of the development process for hard- and software, DAL, SIL)</li> <li>• safety and reliability assessments (top down assessments, functional hazard assessment FHA, bottom up analysis, failure modes and effects analysis FMEA/FMES, fault trees FT, reliability block diagrams RBD, markov processes)</li> <li>• quality management (random sample testing, Weibull curve fitting, HALT/HASS testing)</li> <li>• certification of safety critical systems (safety critical hard- and software, complex electronic hardware, acceptable means of compliance)</li> </ul> |               |              |               |
| Methods           | Class, Lab  |               |              |               |

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|  | <b>Safety and Reliability</b>                           |           |

|              |       |   |     |                             |             |          |
|--------------|-------|---|-----|-----------------------------|-------------|----------|
| Literature   |       | Meyna, A; Pauli, B.: Taschenbuch der Zuverlässigkeits- und Sicherheitstechnik<br>Birnolini, A.: Quality and Reliability of Technical Systems<br>IEC 61508 - Funktionale Sicherheit sicherheitsbezogener elektrischer/elektronischer/programmierbar elektronischer Systeme (sowie IEC 61511, IEC 61513, EN 50128, IEC 62061, IEC 60601, ISO/DIS 26262)<br>DO160, Environmental Conditions and Test Procedures for Airborne Equipment<br>DO178 Software Considerations in Airborne Systems and Equipment Certification<br>ARP4761 Guidelines and Methods for Conducting the Safety Assessment Process on Civil Airborne Systems and Equipment<br>ARP 4754 Certification Considerations for Highly-Integrated Or Complex Aircraft Systems<br>MIL STD 785B Reliability Program for Systems and Equipment Development and Production<br>MIL HDBK 217F Reliability Prediction of Electronic Equipment |     |                             |             |          |
| Exams        |       | written or oral exam  |     |                             |             |          |
| Workload (h) | class | Exercises, / Seminar<br>other   | lab | Home work /<br>presentation | Preparation | Industry |
|              | 15    | 0   | 15  | 15                          | 45          | 0        |
| Language     |       | English   |     |                             |             |          |
| Remarks      |       |   |     |                             |             |          |
| Credits      |       | <b>3</b>  |     |                             |             |          |

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|  | <b>Embedded Systems Project</b>                         | As of: January 2016 |

|                   |   |                              |               |                          |                                     |               |                          |
|-------------------|---|------------------------------|---------------|--------------------------|-------------------------------------|---------------|--------------------------|
| Module Name       | <b>C.11. Embedded Systems Project</b>   |                              |               |                          | Abbreviation                        | <b>ES-PRO</b> |                          |
| Module Group      | Systems   |                              |               | Pflicht                  | <input checked="" type="checkbox"/> | Wahl          | <input type="checkbox"/> |
| Summer / Winter   | Winter  |                              | Semester Term | 2                        |                                     |               |                          |
| Master Programs   | ESD   |                              |               |                          |                                     |               |                          |
| Group             | 15 students   |                              |               |                          |                                     |               |                          |
| Teaching Staff    | K. Peter, M. Lindemann, K. Mueller  |                              |               |                          |                                     |               |                          |
| Person in Charge  | K. Mueller  |                              |               |                          |                                     |               |                          |
| Requirements      |   |                              |               |                          |                                     |               |                          |
| Course Types      | Labor ESD   | 3 h                          | GF = 0,5      |                          |                                     |               |                          |
|                   | Kolloquium  | 1 h                          | GF = 0,5      |                          |                                     |               |                          |
| Course Objectives | <p>The students should learn about the complete development cycle on embedded systems. The embedded design must be fully function on real systems (cart/-pendulum plant, magnetic levitation, cart on beam, pulse oximetry device, thermal generator etc.)</p> <p>Students will know about project management, teamwork, presentation and documenting.</p>  |                              |               |                          |                                     |               |                          |
| Content           | <ul style="list-style-type: none"> <li>• Programmable logic design (DSP, process interface)</li> <li>• Hardware design</li> <li>• Real-time software</li> <li>• GUI programing</li> <li>• System modeling</li> <li>• System simulation</li> <li>• System identification</li> <li>• Legacy C code verification</li> <li>• Technical documentation</li> <li>• Presentation</li> </ul>   |                              |               |                          |                                     |               |                          |
| Methods           | Class, lab, team work, presentation, technical report   |                              |               |                          |                                     |               |                          |
| Literature        | <p>Noergaard, T.: Embedded Systems Architecture: A Comprehensive Guide for Engineers and Programmers (Embedded Technology). Elsevier, 2005</p> <p>Vahid, F. und Givargis, T.: Embedded System Design: A Unified Hardware/Software Introduction. Wiley, 2001</p> <p>Ganssle, J.: The Art of Designing Embedded Systems. Newnes, 2008</p> <p>Siewert, S.: Real-Time Embedded Components and Systems (Computer Engineering). Charles River Media, 2006</p> <p>Berger, A.: Embedded Systems Design: An Introduction to Processes, Tools and Techniques, CMP Books, 2001</p> |                              |               |                          |                                     |               |                          |
| Exams             | Colloquium and Report   |                              |               |                          |                                     |               |                          |
| Workload (h)      | class   | exercises / seminars / other | lab           | home work / presentation | preparation                         | Industry      |                          |
|                   | 0   | 0                            | 45            | 190                      | 125                                 | 0             |                          |

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|  | <b>Embedded Systems Project</b>                         | As of: January 2016 |

|          |           |
|----------|-----------|
| Language | English   |
| Remarks  |           |
| Credits  | <b>12</b> |

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|  | <b>Optional Course</b>                                  | As of: January 2016 |

|                  |  |                              |        |                          |                          |              |                                     |
|------------------|--|------------------------------|--------|--------------------------|--------------------------|--------------|-------------------------------------|
| Module Name      | <b>C.12. Optional Course</b>   |                              |        |                          |                          | Abbreviation | <b>ES-OPT</b>                       |
| Module Group     | Commercial, Soft-Skill, Techniques   |                              |        | mandat.                  | <input type="checkbox"/> | opt.         | <input checked="" type="checkbox"/> |
| Summer / Winter  | Winter   |                              |        | Semester Term            | <b>2</b>                 |              |                                     |
| Master Programs  | ESD, other students of the University of Applied Sciences Bremerhaven  |                              |        |                          |                          |              |                                     |
| Group            | 15 students of ESD, other students, Studium Generale   |                              |        |                          |                          |              |                                     |
| Teaching Staff   | All Professors and lecturers of the University of Applied Sciences Bremerhaven   |                              |        |                          |                          |              |                                     |
| Person in Charge | K. Müller  |                              |        |                          |                          |              |                                     |
| Requirements     |  |                              |        |                          |                          |              |                                     |
| Course Types     | Class, lab, exercise   |                              |        |                          |                          |              |                                     |
| Objective        | In order to individualize their capabilities, each student can select a module, which has 5 credits, of another master program of the University of Applied Sciences Bremerhaven including Studium Generale. |                              |        |                          |                          |              |                                     |
| Content          | Refer to the module description of the selected course   |                              |        |                          |                          |              |                                     |
| Methods          | Class, lab, exercise, group work, seminar  |                              |        |                          |                          |              |                                     |
| Literature       | --   |                              |        |                          |                          |              |                                     |
| exam             | Refer to the module description of the selected course   |                              |        |                          |                          |              |                                     |
| Workload (hours) | class  | exercises / seminars / other | lab    | home work / presentation | preparation              | Industry     |                                     |
|                  | ca. 30   | ca. 15                       | ca. 15 | ca. 20                   | 60                       | 0            |                                     |
| Language         | English  |                              |        |                          |                          |              |                                     |
| Remarks          |  |                              |        |                          |                          |              |                                     |
| Credits          | <b>5</b>   |                              |        |                          |                          |              |                                     |

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|  | <b>Master Thesis</b>                                    | As of: January 2016 |

|                  |  |                               |     |                          |               |                          |                                     |
|------------------|--|-------------------------------|-----|--------------------------|---------------|--------------------------|-------------------------------------|
| Module Name      | <b>C.13. Master Thesis</b>   |                               |     |                          |               | Abbreviation             | <b>MA-ESD</b>                       |
| Module Group     | Master Thesis  |                               |     |                          |               | mandat.                  | <input checked="" type="checkbox"/> |
| Summer / Winter  | Summer   |                               |     |                          | Semester Term | <input type="checkbox"/> |                                     |
| Master Program   | ESD  |                               |     |                          |               |                          |                                     |
| Group            | 15 students  |                               |     |                          |               |                          |                                     |
| Teaching Staff   | A. Bochert, K. Peter, M. Lindemann, K. Müller, other Professors  |                               |     |                          |               |                          |                                     |
| Person in Charge | K. Mueller   |                               |     |                          |               |                          |                                     |
| Requirements     | 40 credits acquired from courses   |                               |     |                          |               |                          |                                     |
| Course Types     | Master Thesis  | 0 h                           |     | GF = 0,8                 |               |                          |                                     |
|                  | Colloquium   | 4 h                           |     | GF = 0,2                 |               |                          |                                     |
| Objectives       | The students should verify that they could solve complex design tasks with scientific methods at master level. |                               |     |                          |               |                          |                                     |
| Content          | The content depends on the scientific or industrial assignment.  |                               |     |                          |               |                          |                                     |
| Methods          | Self-contained research and development, individual support by supervisors.                                    |                               |     |                          |               |                          |                                     |
| Literature       |  |                               |     |                          |               |                          |                                     |
| Exams            | Written Master Thesis, Colloquium  |                               |     |                          |               |                          |                                     |
| Workload (h)     | class  | Exercises / seminars / others | lab | home work / presentation | preparation   | Industry                 |                                     |
|                  | 0  | 0                             | 0   | 900                      | 0             | 0                        |                                     |
| Language         | German / English   |                               |     |                          |               |                          |                                     |
| Remarks          |  |                               |     |                          |               |                          |                                     |
| Credits          | <b>30</b>  |                               |     |                          |               |                          |                                     |