

PicoBlaze™ Port Definitions

Post definitions coded in VHDL:

Adresse [hex]	INPUT	OUTPUT
00	UART_status_port	LED_port
01	UART_read_port	UART_write_port
02	LCD_input_port	LCD_output_port
03	BUTTONSWITCH_port	--
04	EXTERN_port	EXTERN_port

UART_status_port (RD, 0x00):

7	6	5	4	3	2	1	0
–	–	–	RX data present	RX full	RX half full	TX full	TX half full

UART_read_port (RD, 0x01):

7	6	5	4	3	2	1	0
RX7	RX6	RX5	RX4	RX3	RX2	RX1	RX0

LCD_input_port (RD, 0x02): [2 x 16 character LCD]

7	6	5	4	3	2	1	0
LCRD7	LCRD6	LCRD5	LCRD4	LCRS3	LCRS2	LCRS1	LCRS0

BUTTONSWITCH_port (RD, 0x03):

7	6	5	4	3	2	1	0
BTNW	BTNN	BTNS	BTNE	SW3	SW2	SW1	SW0

LED_port (WR, 0x00):

7	6	5	4	3	2	1	0
LED7	LED6	LED5	LED4	LED3	LED2	LED1	LED0

UART_write_port (WR, 0x01):

7	6	5	4	3	2	1	0
TX7	TX6	TX5	TX4	TX3	TX2	TX1	TX0

LCD_output_port (WR, 0x02) [2 x 16 character LCD]

7	6	5	4	3	2	1	0
LCWR7	LCWR6	LCWR5	LCWR4	LC_drv	LC_RS	LC_RW	LC_E

EXTERN_port (RD, 0x04) [external port]

7	6	5	4	3	2	1	0
X	X	X	X	X	X	EXIN1 [A4]	EXIN0 [B4]

EXTERN_port (WR, 0x04) [external port]

7	6	5	4	3	2	1	0
X	X	X	X	X	X	EXOUT1 [C5]	EXOUT0 [D5]
